

中文



STATE INTELLECTUAL PROPERTY OFFICE OF THE P.R.C.

[HOME](#) [ABOUT SIVO](#) [NEWS](#) [LAW&POLICY](#) [SPECIAL TOPIC](#) [CHINA IP NEWS](#)**Title: System chip structure and operating method having no waiting period****Application Number** 03109017      **Application Date** 2003.04.01**Publication Number** 1448849      **Publication Date** 2003.10.15**Priority Information****International Classification** G06F12/10**Applicant(s) Name** VIA Technologies, Inc.**Address****Inventor(s) Name** Chen Zhiyong;Lin Kunlong**Patent Agency Code** 11219      **Patent Agent** chen xiaomei wen qi**Abstract**

The present invention relates to one kind of control chip and its data access method. The control chip includes one CPU and one interface control circuit, and the interface control circuit is used to convert the internal data access address and corresponds to one external data buffer memory area, so the CPU can access directly in the external data buffer memory area. The data access method includes detecting the internal data access address in the internal memory area, converting and corresponding the detected address to an external data memory area address, sending request for the control chip to access data in external data memory area, pausing in the data access of the control chip before the request is responded, and answering the control chip for data access in external data memory area after request is responded.

[Machine Translation](#)[Close](#)